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implementation of a
high-precision VLSI
winner-take-all (WTA)
circuit that can be
arranged to process
1024 inputs are

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presented. The
cascade configuration
can be used to
significantly ...

A high-precision VLSI winner-take-all circuit for self- organizing neural networks

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startup Impinj Inc. has
developed an
innovative technology
for bridging the gap
between precision
analog and faster ...

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of VLSI design, Mead
also

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Precision Analog In
Digital CMOS**

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where million of
transistors are
fabricated and
connected using
mostly software tools
configured/scripted or
programmed by
analog, digital circuit ...

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Most of the electronics

...

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Advances in very-large-
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... rate is often fixed by

design (Fig. 3). Such a

set of curves for the

AD9762, a 12-bit

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Devices CMOS TxDAC

family, are shown ...

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for analog filter
designs, as well as for
advanced ...

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Continuous Time

Active Analog Filters

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algorithm produced a
circuit nearly identical

to the description of a

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**augmented neural
networks**

Studer, C. Blosch, P.
Friedli, P. and Burg, A.
2007. Matrix

Decomposition

Architecture for MIMO
Systems: Design and

Implementation Trade-
offs. p. 1986.

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by the analog
operation. Therefore,
they are not suitable
for a ...

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its benefits when ...

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technology (Figure 55
... are of interest for
VLSI in addition to
applications on
alternative ...

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